



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

(Case No. RA043D2C3C2)

In the Application of:

FARMWALD ET AL.

Serial No: 09/779,296

Filed: February 8, 2001

Title: MEMORY DEVICE HAVING A
VARIABLE DATA OUTPUT LENGTH

Assistant Commissioner for Patents
Washington, DC 20231

Group Art Unit: 2818

Before

Examiner: T. Nguyen

I hereby certify that this
correspondence is being deposited
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Patents and Trademarks, Washington,
D.C. 20231 on Aug 9, 2001

Jose G. Moniz
(Name of Person Mailing
Correspondence)

[Signature] 8-9-01
Signature Date

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. §1.56, submitted herewith is a modified Form PTO-1449, including a copy of all of the documents listed therein.

Pursuant to 37 C.F.R. §1.97(c), the Commissioner is hereby authorized to charge the fee set forth in 37 C.F.R. §1.17(b), in the amount of \$ 180.00 to Applicants' Deposit Account No. 50-09998. A duplicate copy of this sheet is enclosed.

It is believed that the Examiner may find the documents cited in the modified Form PTO-1449 material to the patentability of one or more of the claims in the above-captioned application. Accordingly, it is respectfully requested that the Examiner make his consideration of these references formally of record with the initial Office Action.

It should be noted that three (3) of the documents listed in the PTO-1449 have been brought to the Applicants' attention by way of the plaintiff in a proceeding pending in U.S. District

2818

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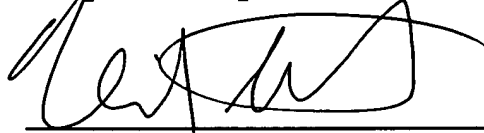
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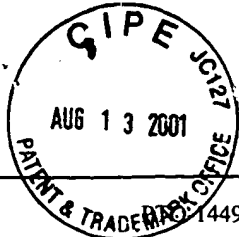
Court for the District of Delaware, namely in Micron Technology Inc. v. Rambus Inc. The plaintiff identified the documents as prior art against the inventions claimed in, among others, U.S. Patent 6,034,918. The '918 Patent is the grandparent patent of the instant Application. The three documents are explicitly listed on pages 40-41 of Appendix B - Micron Technology's Supplemental Response to Interrogatory No. 5 (hereinafter "SUPPLEMENTAL RESPONSE"). A copy of the SUPPLEMENTAL RESPONSE is enclosed herewith.

Date: August 8, 2001

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Neil A. Steinberg', written over a horizontal line.

Neil A. Steinberg
Reg. No. 34,735
650-947-5325



Sheet 1 of 1

NO. 1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. RA043D2C3C2	SERIAL NUMBER 09/779,296
	APPLICANT(S) FARMWALD ET AL.	
	FILING DATE FEBRUARY 8, 2001	GROUP ART UNIT 2818

DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	"MIPS Chip Set Implements Full ECL CPU" Microprocessor Report, MicroDesign Resources Inc., Vol. 3: No. 12; Dec. 1989
	"R6000 System Bus & R6020 SBC Specification" MIPS Computer Systems Inc., Sunnyvale, CA, Aug 22, 1989
	R.A. Volz et al., "POSITION PAPER ON GLOBAL CLOCK FOR THE FUTUREBUS +", SCI - 1989 - doc-59, pp. 1-9
	"ECL bus controller hits 266 Mbytes/s" Microprocessor Report, MicroDesign Resources Inc., Vol. 4: No. 1; Pg. 12, Jan. 24, 1990

EXAMINER	DATE CONSIDERED
EXAMINER: Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant.	